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L14 and cache	12	

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IBM Technical Disclosure Bulletins

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Search History

DATE: Tuesday, June 01, 2004 Printable Copy Create Case

Set Name side by side	Query	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set
DB=I	PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ		
<u>L15</u>	L14 and cache	12	<u>L15</u>
<u>L14</u>	L12 same graphic\$3 near3 data	19	<u>L14</u>
<u>L13</u>	L12 same graphic\$3 same internal same external	1	<u>L13</u>
<u>L12</u>	(shar\$3 or unif\$4)near3 (cache or memory or buffer) near8 (chipset or chip or hub or core)	1374	<u>L12</u>
<u>L11</u>	(shar\$3 or unif\$4)near3 (cache or memory or buffer) same internal near3 proces\$4 same external near3 proces\$4	59	<u>L11</u>
<u>L10</u>	(shar\$3 or unif\$4)near33 (cache or memory or buffer) same internal near3 proces\$4 same external near3 proces\$4	80	<u>L10</u>
<u>L9</u>	unif\$6 adj3 (cache) same internal near3 proces\$4 same external near3 proces\$4	9	<u>L9</u>
<u>L8</u>	unif\$5 adj3 (cache or memory or buffer) same internal near3 proces\$4 same external near3 proces\$4	11	<u>L8</u>

<u>L7</u>	unify\$3 adj3 (cache) same internal near3 proces\$4 same external near3 proces\$4	0	<u>L7</u>
<u>L6</u>	shar\$3 adj3 (cache) same internal near3 proces\$4 same external near3 proces\$4	2	<u>L6</u>
<u>L5</u>	13 same addres\$4	38	<u>L5</u>
<u>L4</u>	13 same physical	0	<u>L4</u>
<u>L3</u>	11 same graphic\$3	88	<u>L3</u>
<u>L2</u>	shar\$3 adj3 (cache or memory or buffer) same internal near3 proces\$4 same external near3 proces\$4	28	<u>L2</u>
<u>L1</u>	shar\$3 adj3 (cache or memory or buffer) same internal same external	507	<u>L1</u>

END OF SEARCH HISTORY